

MARLUG Agenda | Thursday - May 21, 2009

7:30am - 8:30am	Registration/Continental Breakfast		
8:30am - 8:45am	Opening Remarks		
9:00am - 10:00am	Keynote: Challenges for Designer in Uncertain Times - Happy Holden, Mentor Graphics		
	PCB	High-Speed	FPGA/PCB
	Room 3	Room 5	Room 7
10:00am-11:00am	DxDesigner Joins the Big Leagues - Russ Brown, Northrop Grumman	Understanding Power Integrity in System Designs - Zhen Mu, Mentor Graphics	Manufacturing Compliance: It's YOUR Job! - Happy Holden, Mentor Graphics
11:00am - 12:00pm	A PCB AutoRouting Methodology for Dense Boards that Contain High-Speed Constraints - Mitra Geeban, Mentor Graphics	Xilinx Virtex-5 Rocket I/O Lab/Sim Correlation with HyperLynx BoardSim - Howard Ireland, Xilinx	Reducing Schedules by Restoring an Efficient Balance Between Simulation and Validation - Bob Oden, Mentor Graphics
12:00pm - 1:00pm	Lunch		
1:00pm - 2:00pm	A True Co-Design Flow for RF/Microwave PCBA True Co-Design Flow For RF/Microwave PCB - Dr. Michael Heimlick, AWR Corp.	S-Parameters in Signal Integrity Simulation - Rula Bakleh, L-3 Communications	Techniques to Minimize the Impact of FPGA Life Cycle Management - Dominic Lucido, Mentor Graphics
2:00pm - 3:00pm	Case Study: HDI Stackups & Routing with Microvias - Steve Calder, Northrop Grumman	Understanding Power Distribution Design Using Hyperlynx® 8.0 PI - Alex Golian, Northrup Grumman	