

**Wednesday - May 28, 2008**

7:30am - 8:30am	<b>Registration/Continental Breakfast</b>			
8:30am - 8:45am	<b>Opening Remarks</b>			
9:00am - 10:00am	<b>Keynote: PCB Technology Trends - Happy Holden, Mentor Graphics</b>			
	<b>PADS</b>	<b>EXPEDITION</b>	<b>HIGH-SPEED</b>	<b>FPGA/Verification</b>
	<b>Room 1</b>	<b>Room 3</b>	<b>Room 5</b>	<b>Room 7</b>
10:00am-11:00am	Using HyperLynx in the DxD/PADS Flow - Ernie Frohring, Trilogic	Exchanging IDF Data Between Expedition & Unigraphics - Kelli Hosier, Northrop	Designing High Speed Serial Links: An Overview - Alex Golian, Northrop	DO254 Presentation - Bob Oden, Mentor Graphics
11:00am - 12:00pm	Using I/O Designer for the Flat Designs - Mike Ashbaugh, Trilogic	EE2007 Update - Mitra Geeban, Mentor Graphics	Double Data Rate 2 (DDR2) - Pat Carrier, Mentor Graphics	
12:00pm - 1:00pm	<b>LUNCH</b>			
1:00pm - 2:00pm	PADS 2007.3 Update - Ernie Frohring, Trilogic	Expedition MCM-D Layout for Multi-Layer Die - Mark Geisler, Charles Stark Draper Laboratory	SystemVision™ - Bill Hargin, Mentor Graphics	
2:00pm - 3:00pm	<b>Electrical Performance &amp; Signal Integrity of HDI Structures</b> - Happy Holden, Mentor Graphics			
3:00pm - 4:00pm	<b>High-Speed Update</b> - Pat Carrier, Mentor Graphics			