

Thank you to all those that submitted a paper for the 2007 Best Paper Award at User2User. We would like to congratulate and recognize the following winners in each track. Awards were determined by the technical content of the papers combined with the attendee's evaluations of their presentation. Each of these winners was recognized with a \$1000 cash award.

Board Station

Board Station RE "Fastrack" Autoroute Tutorial
Mike Valle, Shure

Board Station & Expedition

CES, Getting Beyond the Basics
Jayson Harames, L-3 Communications

Calibre

Implementation of a DFM Checker for 65nm and Beyond
Patrick Le Maitre, NXP Crolles2Alliance

Custom ICL-AMS

Designing a RF System Package with an Improved IC Design Environment
Mathieu Behaghel, STMicroelectronics

Design Environment

Driving Using Your Dashboard-Does 'Design Environment' Mean Anything Anymore?
Russ Brown, Northrop Grumman

Design-for-Test

Improvement of At-speed ATPG Quality Using Launch-off-Shift Method
Yoichi Maeda, Renesas Technology America, Inc.

DxDesigner

Changing Attribute Values in a Schematic Using a Vbscript
Tony John, Rockwell Collins

Expedition

Reuse Blocks in the Expedition Flow
Steven Trasatto, Sony Ericsson Mobile

FPGA

Using SystemVerilog for Designing FPGAs
Mark Curry, Anchor Bay Technologies

Functional Verification

Shadow Model and Coverage Driven Processor Verification Using SystemVerilog
Arthur Freitas, Hyperstone

High-Speed

Timing & ICX - Chapter 2
Jim Peterson, Honeywell

PADS

Unleash the Power of PADS
Denis Lefebvre, Finisar Corp.